

FIG. 2D

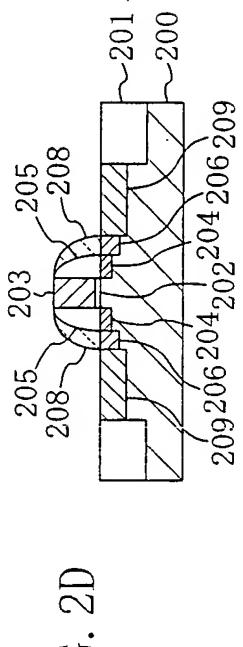


FIG. 2E

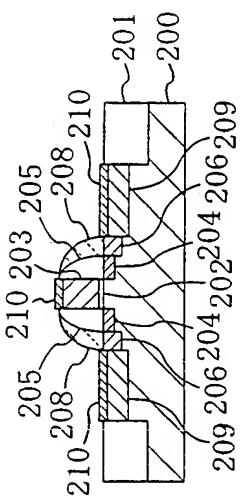


FIG. 2F

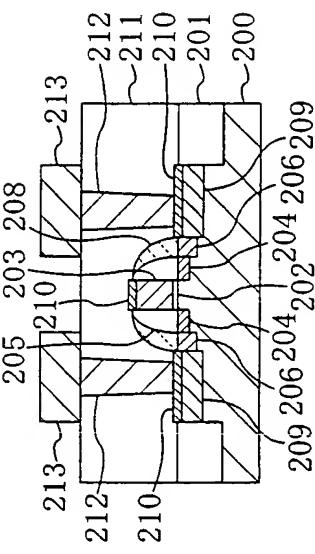


FIG. 3A

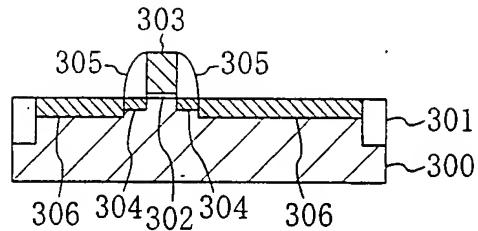


FIG. 3B

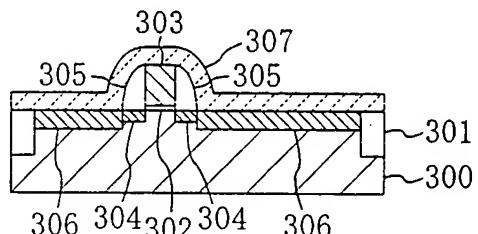


FIG. 3C

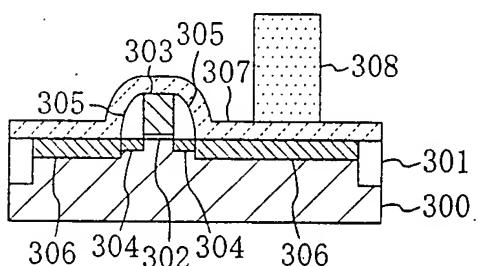


FIG. 3D

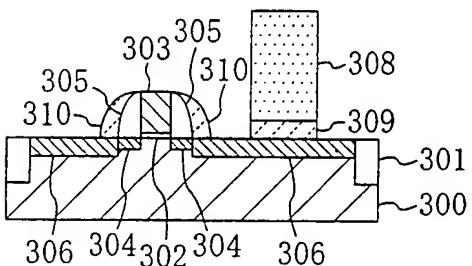


FIG. 4A

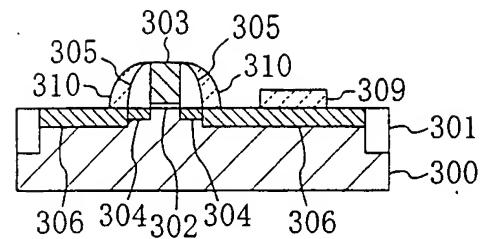


FIG. 4B

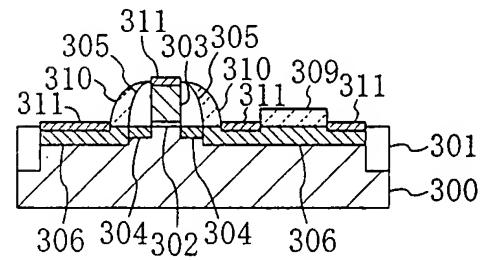
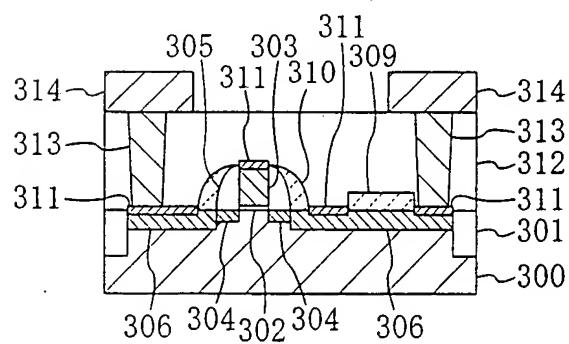


FIG. 4C



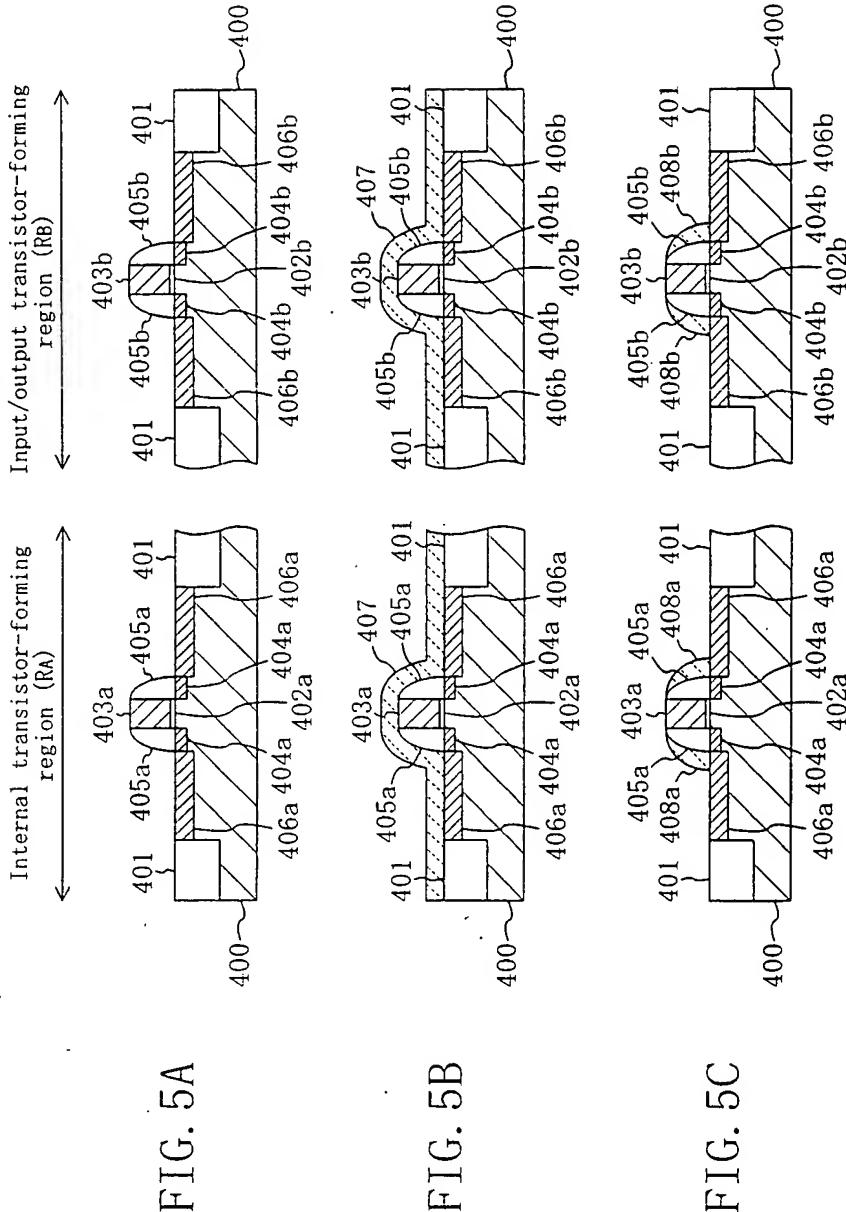


FIG. 6A

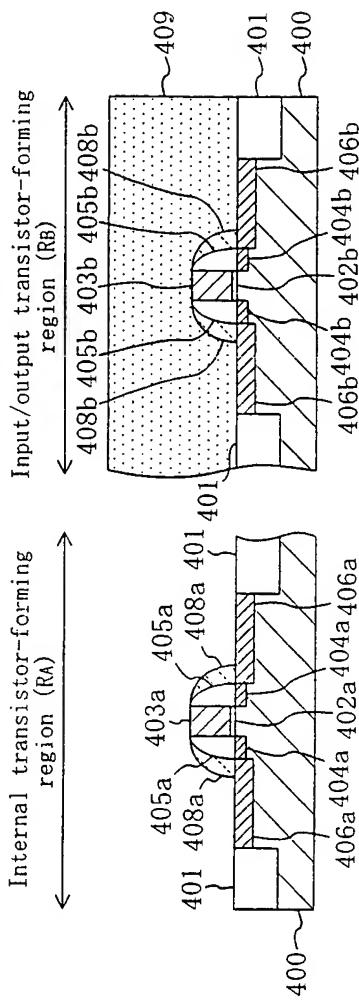


FIG. 6B

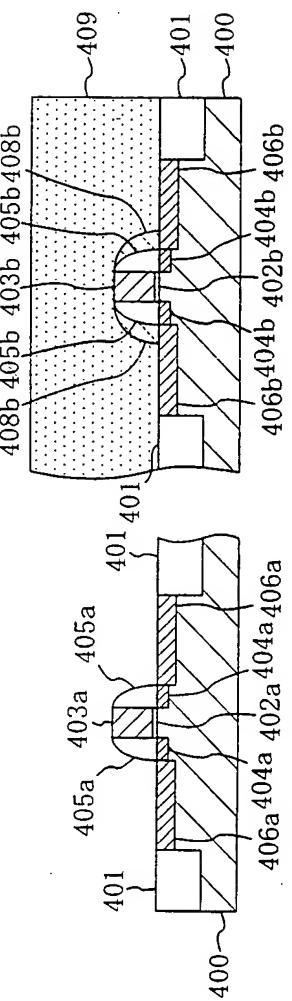
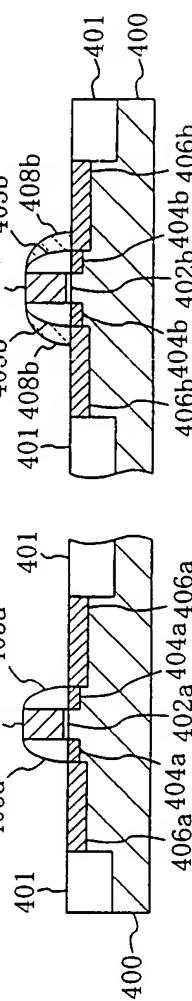
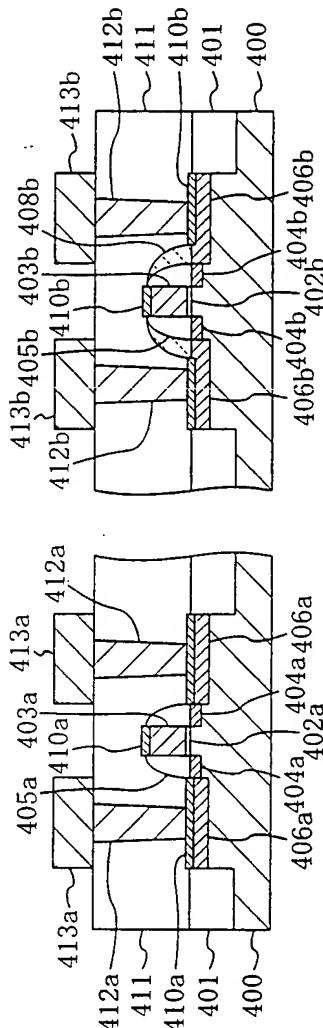
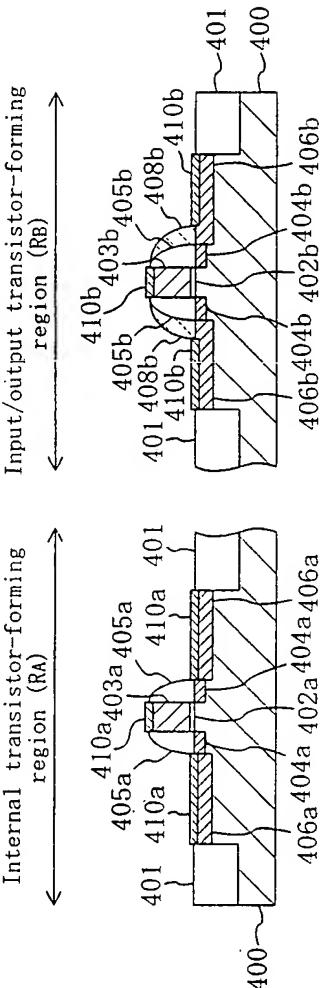


FIG. 6C





Internal transistor-forming region (RA) ← → Input/output transistor-forming region (RB)

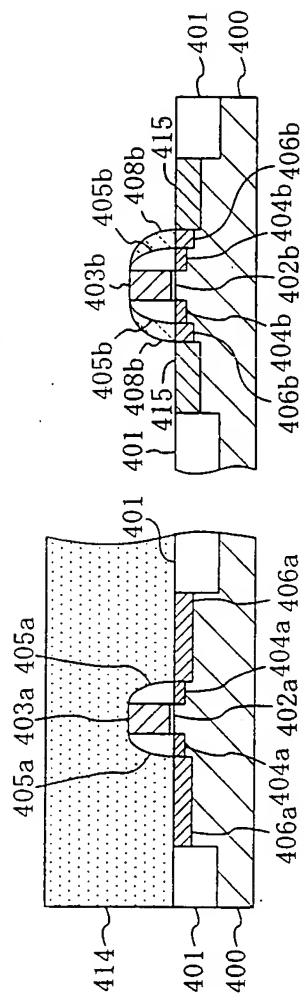
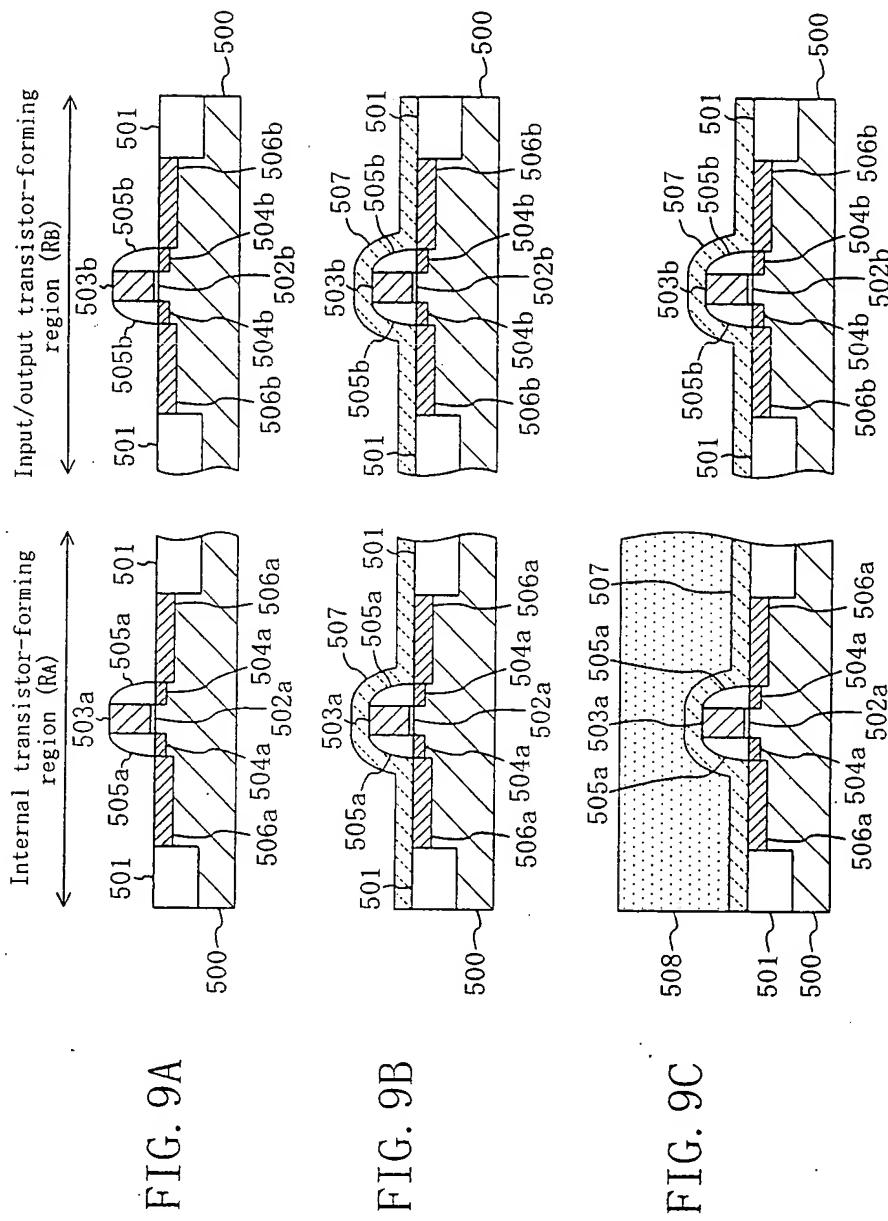


FIG. 8



Internal transistor-forming
region (RA) ← → Input/output transistor-forming
region (RB)

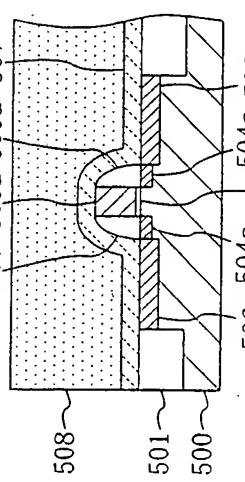


FIG. 10A

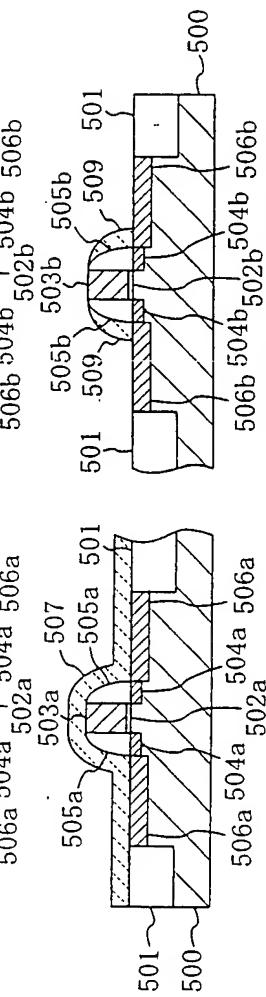


FIG. 10B

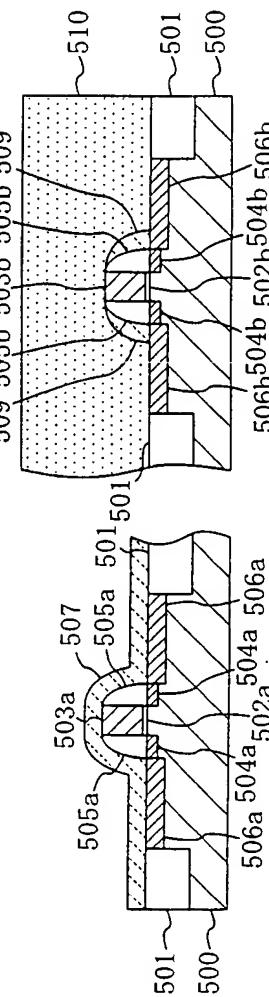
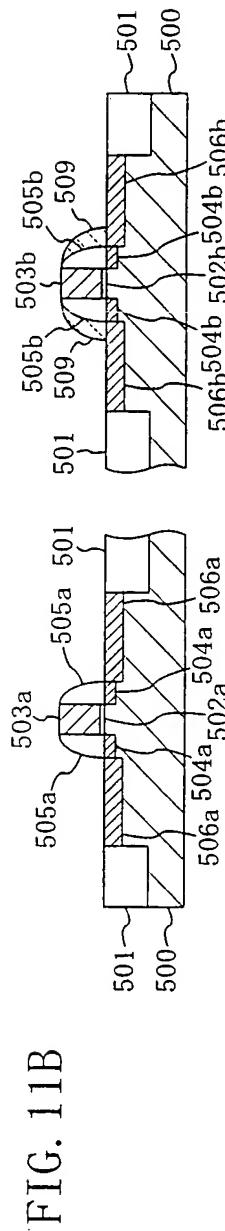
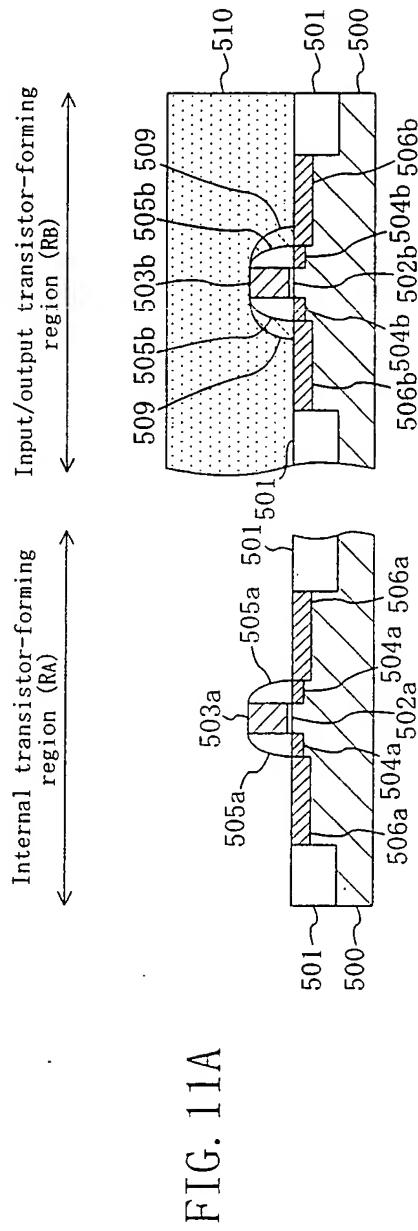
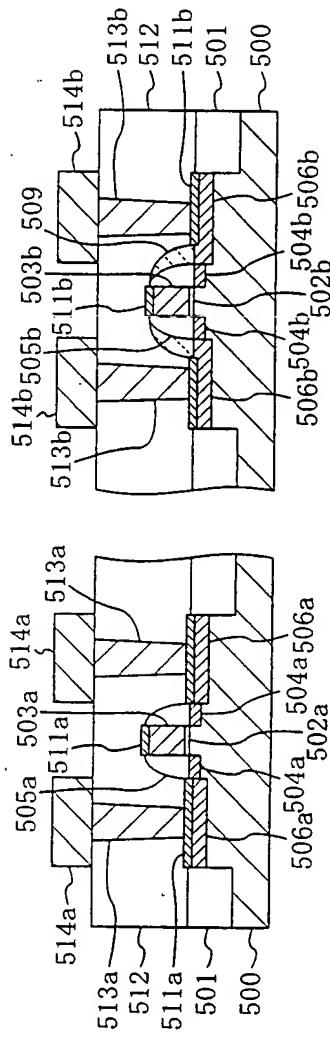
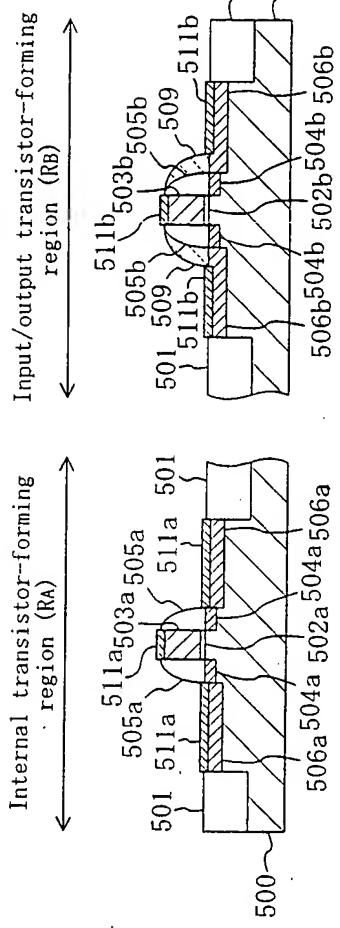


FIG. 10C





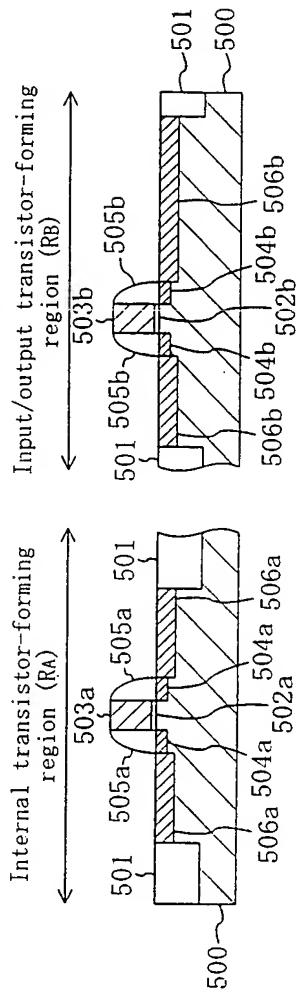


FIG. 13A

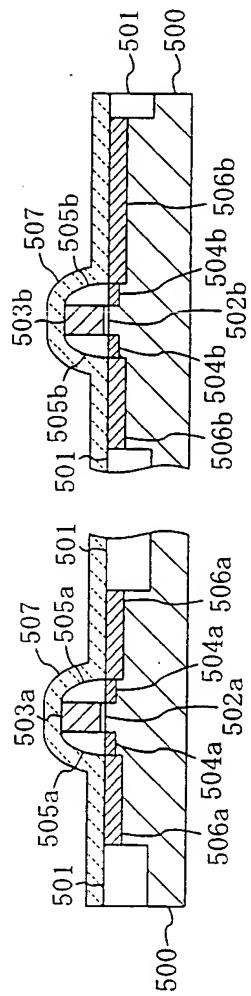


FIG. 13B

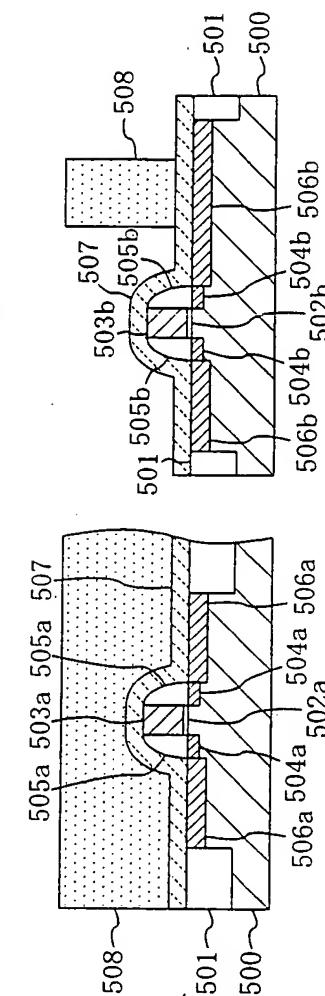
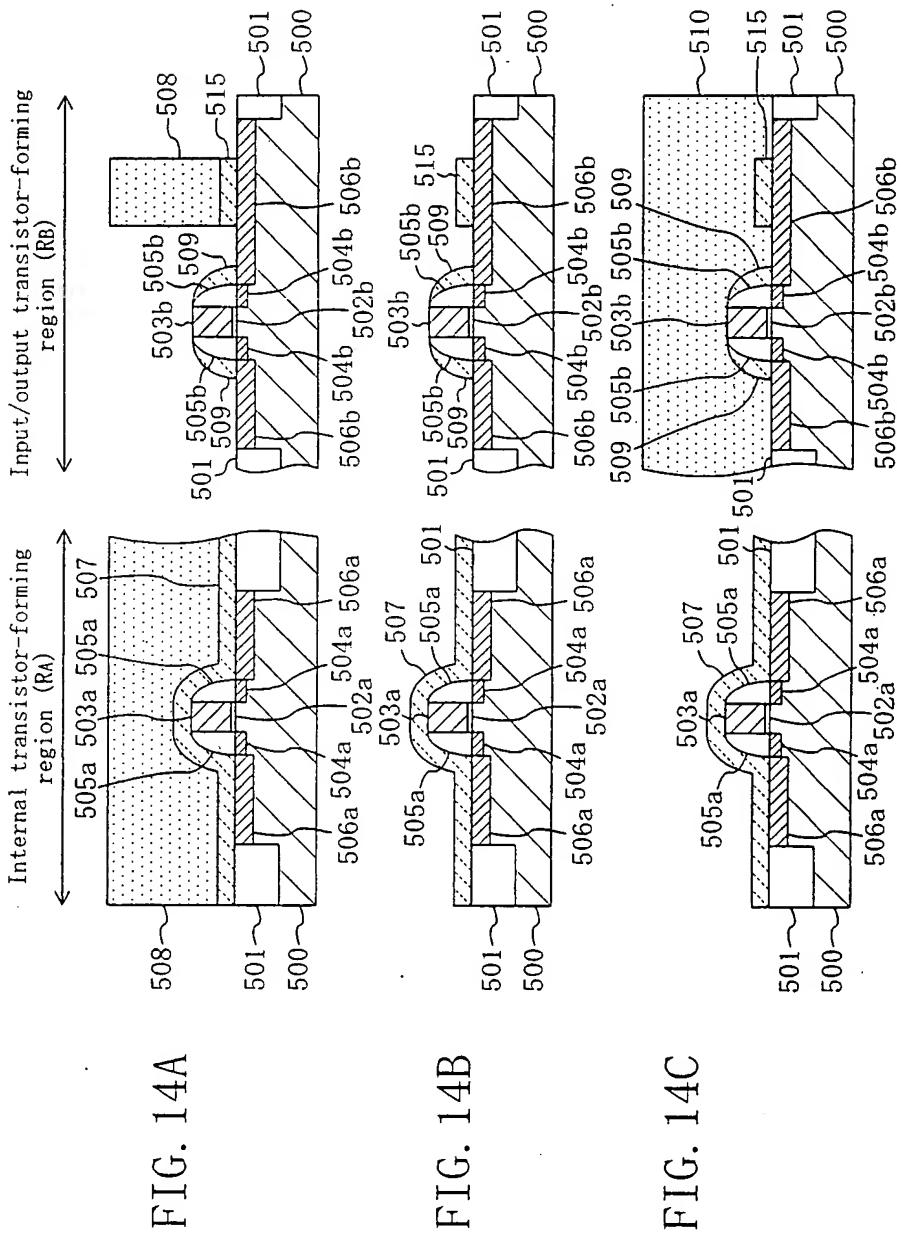
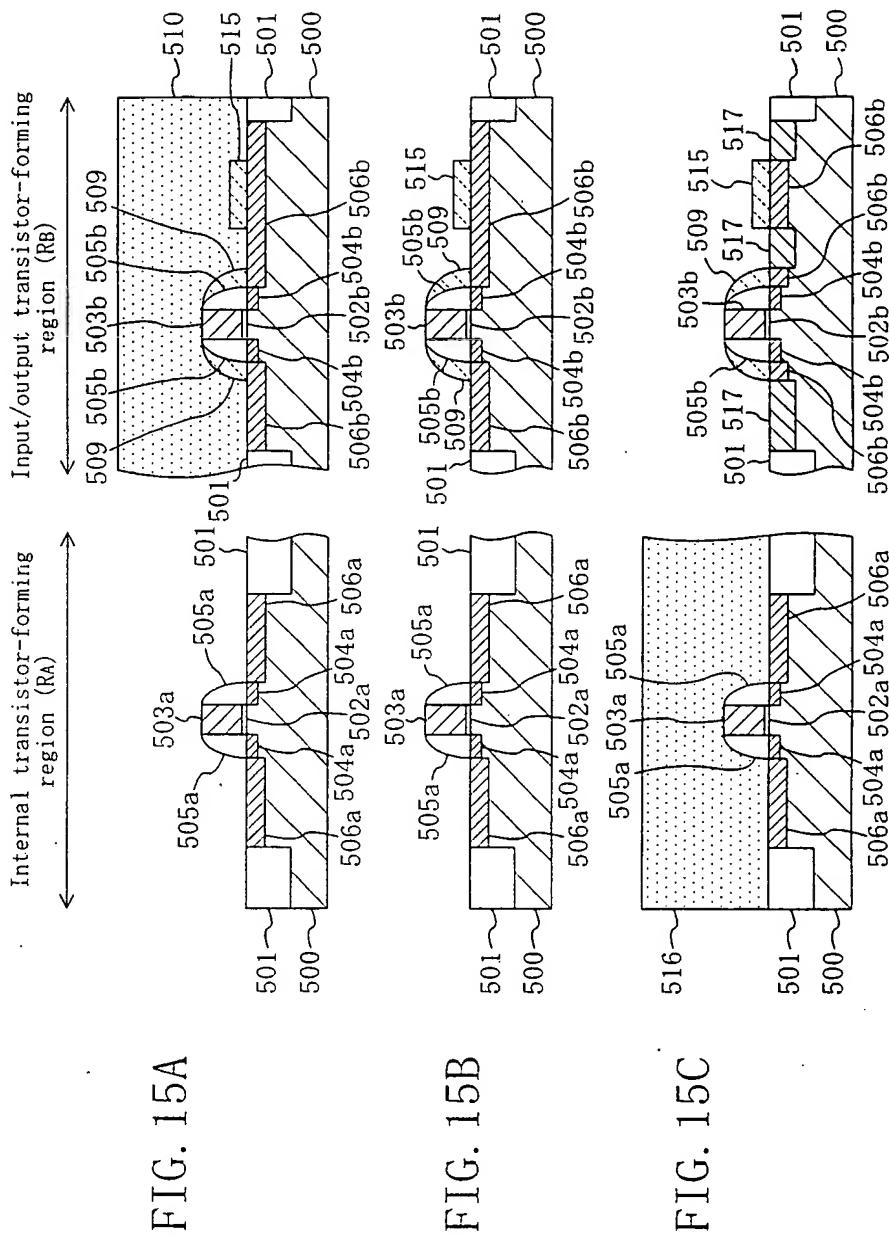


FIG. 13C





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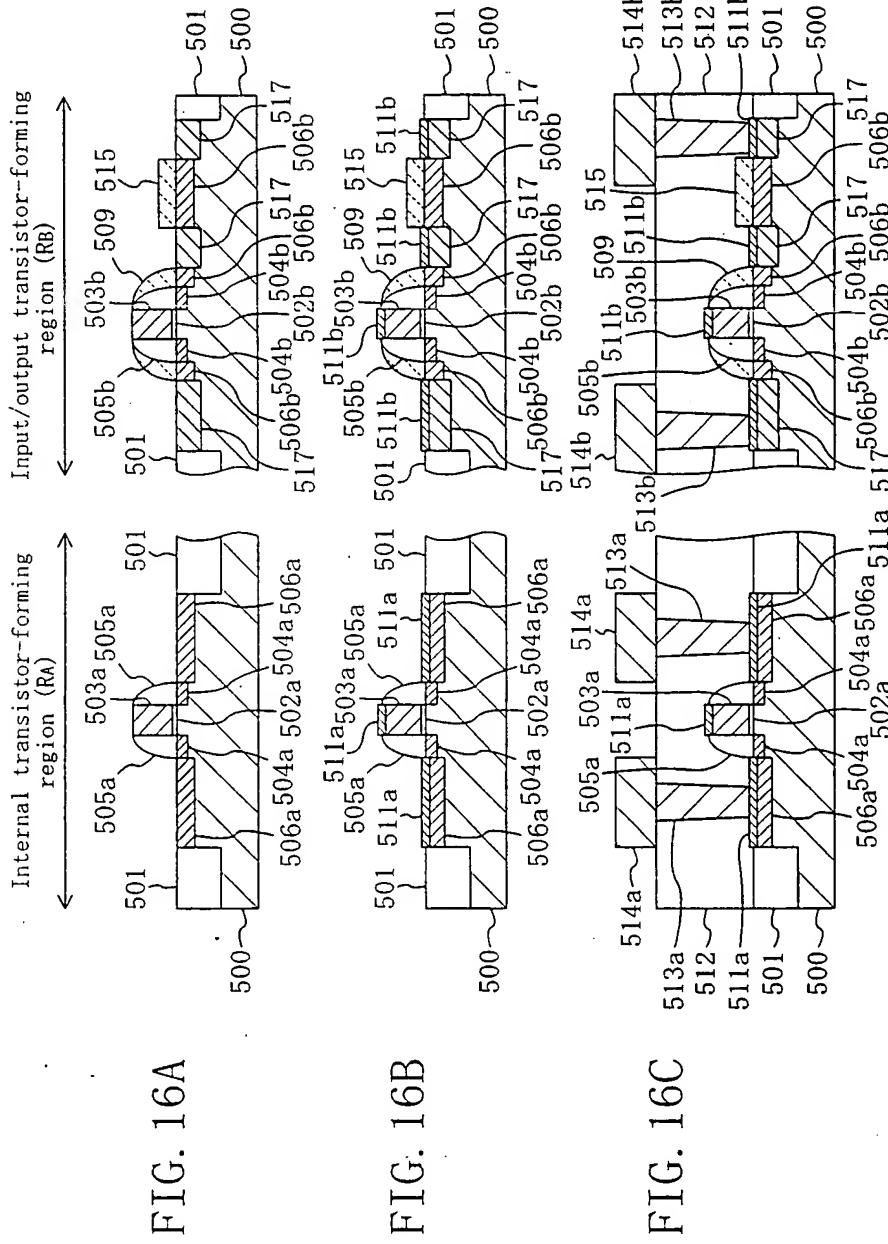


FIG. 17A

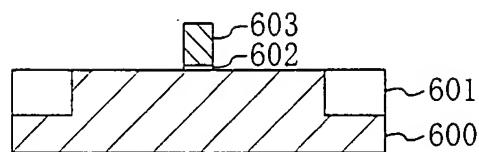


FIG. 17B

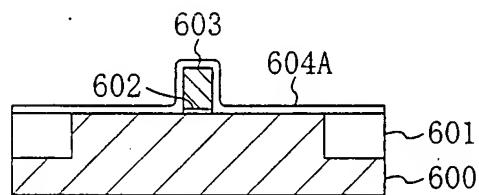


FIG. 17C

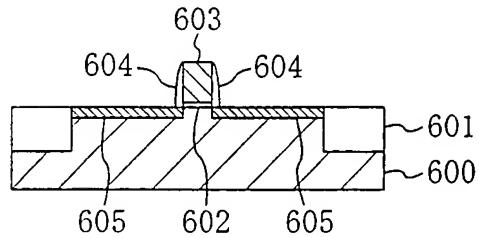


FIG. 17D

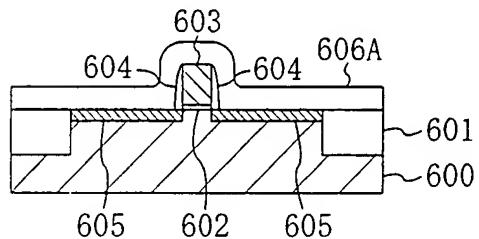


FIG. 18A

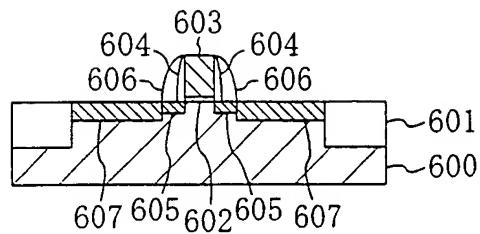


FIG. 18B

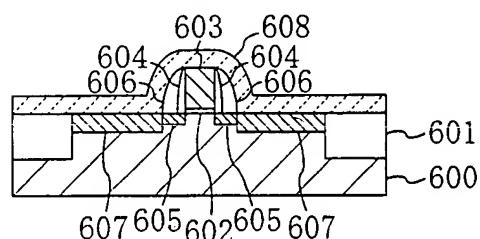


FIG. 18C

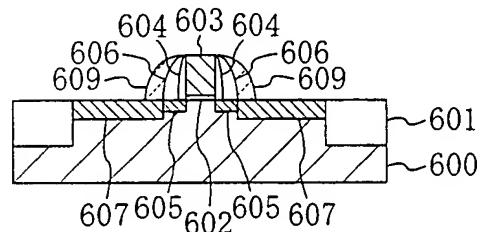


FIG. 18D

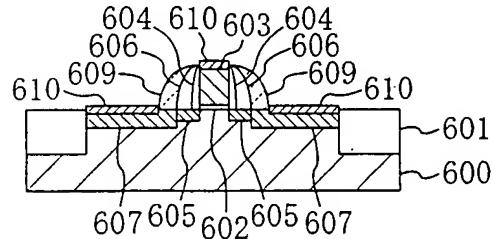
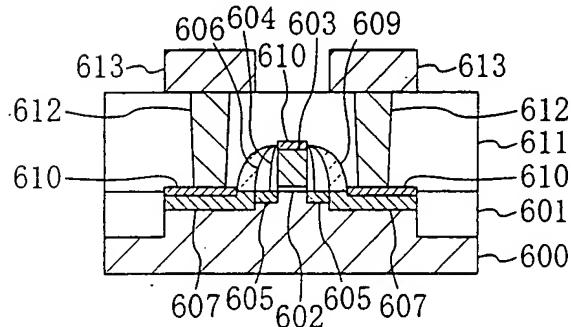


FIG. 18E



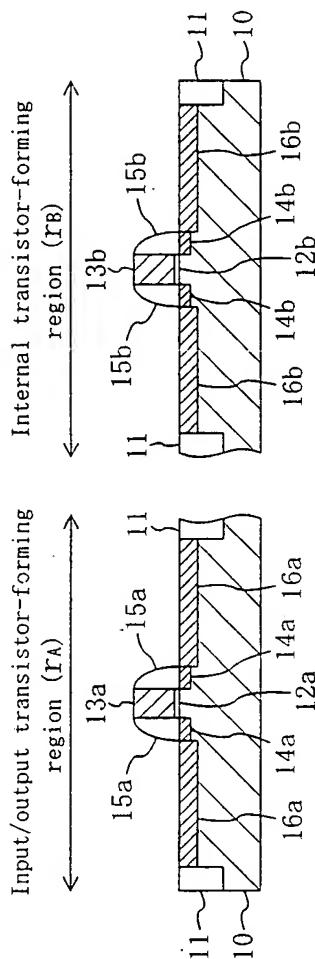


FIG. 19A
Prior Art

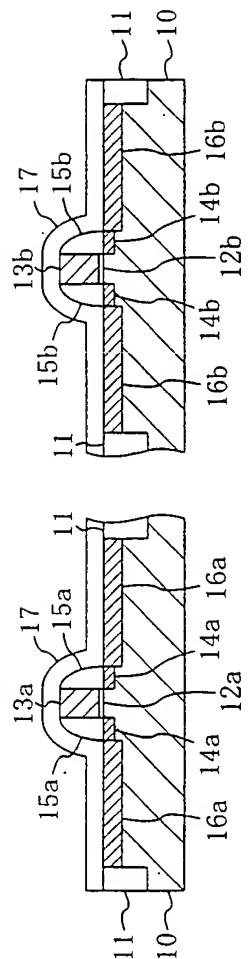


FIG. 19B
Prior Art

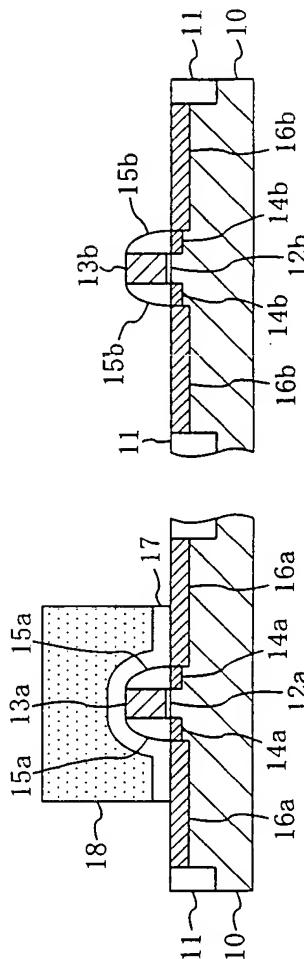


FIG. 19C
Prior Art

